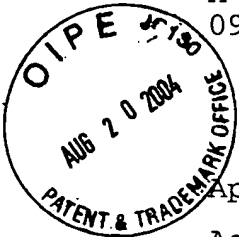


X-786 US  
09/757,404

41 2123  
PATENT  
Conf. No.: 6382



IN THE UNITED STATES PATENT OFFICE

Applicants: Steven A. Guccione et al.  
Assignee: Xilinx, Inc.  
Title: "Method and System for Device-Level Simulation of  
a Circuit Design for a Programmable Logic Device"  
Ser. No.: 09/757,404 File Date: 01/08/2001  
Examiner: Mary C. Hogan Art Unit: 2123  
Docket No.: X-786 US Conf. No.: 6382

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Alexandria, VA 22313-1450

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AUG 26 2004

Technology Center 2100

AMENDMENT IN RESPONSE TO FIRST OFFICE ACTION

Dear Sir:

In response to the Office Action dated June 3, 2004,  
reconsideration and allowance of all claims are respectfully  
requested in view of the remarks that follow.

**Amendments to the Claims** are reflected in the listing of claims  
which begins on page 2 of this paper.

**Amendments to the Drawings** begin on page 7 of this paper and  
include a replacement sheet.

**Remarks** begin on page 8 of this paper.

An **Appendix** including an amended drawing figure (FIG. 1) is  
attached following page 11.